

Science and Technology of Materials, Interfaces, and Processing

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2024 Fall Meeting Hudson Mohawk AVS Chapter Thursday, October 17, 2024 3:30 – 7:30 PM

University at Albany
ETEC building, Room B010
1220 Washington Avenue, Albany, NY 12226

Meeting Agenda

3:30 - 3:50 Reception (coffee and cookies served)3:50 - 4:00 Welcoming Remarks

Keynote Presentation

4:00 - 4:30 Exploring Topological Conductors for Post-Cu Interconnects

Dr. Ching-Tzu Chen (IBM T.J. Watson Research Center)

Oral Presentations

4:30 - 4:50 Investigation of Low Temperature Fluorine-Based Etching Of TaN With Selectivity To SiOCH Dielectric, Ivo V. Otto IV (UAlbany)
4:50 - 5:10 Electronic Structure, Secondary Electron Yields, And Chemical Transformations of Photoresist Materials, Dr. Sylvie Rangan (Rutgers)
5:10 - 5:30 Degradation of Organosilicon-Functionalized Surfaces During X-Ray Photoelectron Spectroscopy: An In-Depth Study, Anthony Valenti (UAlbany)
5:30 - 7:00 Poster Presentations (pizza and refreshments served)

7:00 - 7:20 Student Awards Ceremony

- P1. MANGANESE BORIDE AS POTENTIAL INTERCONNECT METAL

 Sanzida Rahman (Rensselaer Polytechnic Institute)
- P2. **MEASUREMENT OF THE DYNAMIC RESPONSE OF MEMS CANTILEVERS**<u>Alvar Garza</u> (University at Albany)
- P3. ANGLE-RESOLVED PHOTOLUMINESCENCE SPECTROSCOPY OF ERBIUM-IMPLANTED THIN-FILMS AND NANOPHOTONIC STRUCTURES

 Souryaya Dutta, Blair Garrett (University at Albany)
- P4. ENHANCING PLASMONIC PHOTODETECTORS: A STUDY ON THE FABRICATION AND OPTICAL BEHAVIOR OF NANOANTENNAS

 Maryam Ahmadi (University of Connecticut)
- P5. DEVELOPMENT OF QUANTUM DOT SCINTILLATOR WITH INTEGRATED PHOTODETECTOR FOR ENHANCED RADIATION DETECTION

 Gyana Biswal (University at Albany)
- P6. CuAl₂ AS CONDUCTOR FOR HIGH-CONDUCTIVITY INTERCONNECTS

 Zahra Ahmadian (Rensselaer Polytechnic Institute)

Driving Directions

The ETEC building is in the Harriman State Office Complex, which is adjacent to the University at Albany campus. The address of the ETEC building is ETEC, 1220 Washington Avenue, Albany, NY 12226. It is important that you include "ETEC" in your search if you are using a mapping program; otherwise, the search will send you to the state police barracks.

From UAlbany Campus:

There is no direct street access to the ETEC building from the UAlbany campus. You must first drive to either Washington Avenue or Western Avenue and go east towards downtown Albany. Just after passing the UAlbany campus, there will be an entrance to the Harriman State Office Complex (State Office Buildings).

- If entering Campus Access Road from Washington Avenue, you will see the ETEC building on your left. After passing the building, you will take a U-turn to access the side of Campus Access Road that passes in front of the ETEC building. The parking lot is on the north side of the building.
- If entering Campus Access Road from Western Avenue, you will also need to take a U-turn so that you are driving towards the ETEC building, not away from it. Again, the parking lot is on the north side of the ETEC building.

From the North:

Take the Northway (I-87) south to Exit 1E. Merge onto I-90 east (toward Albany/Boston). Then take Exit 3 (State Office Buildings), which will put you on Campus Access Road. After passing the ETEC building, you will take a U-turn to access the side of Campus Access Road that passes in front of the ETEC building.

From the **South**:

Take the Thruway (I-87) north to Exit 24. Merge onto I-90 east (toward Albany/Boston). Then take Exit 3 (State Office Buildings), which will put you on Campus Access Road. After passing the ETEC building, you will take a U-turn to access the side of Campus Access Road that passes in front of the ETEC building.

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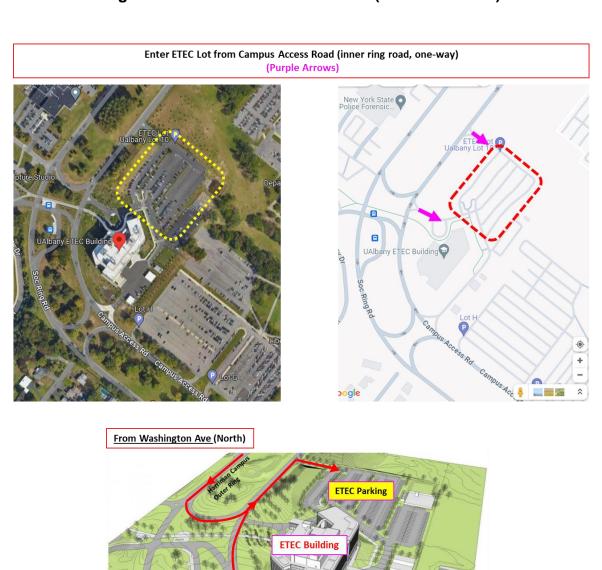
Take the I-90 west to Exit 3 (State Office Buildings), which will put you on Campus Access Road. After passing the ETEC building, you will take a U-turn to access the side of Campus Access Road that passes in front of the ETEC building.

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HMAVS meeting location is in ETEC Room B010 (Basement level).



From Western Ave (US-20, South)

Keynote Presentation:

EXPLORING TOPOLOGICAL CONDUCTORS FOR POST-CU INTERCONNECTS

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Surface-state conduction in topological conductors yields unconventional resistivity scaling, such that resistivity decreases with reduced sample dimensions down to ~nm. This may provide a solution to the interconnect bottleneck in highly scaled integrated circuits. In this talk, we first review the theoretical electrical transport investigations of two prototypical topological semimetals, CoSi and NbAs, which provides insights into the resistivity scaling in topological conductors with and without defects. We then review the experimental data of CoSi thin films and wafer-scale nanowire devices down to ~5 nm scale. The magneto-transport measurements reveal coexisting high-mobility surface carriers with low-mobility bulk carriers. Most notably, we observe that room-temperature resistivity in nanoscale CoSi films can drop below the ideal bulk single-crystal limit, and the resistivity in highly textured CoSi nanowires reduces with the channel width. These proof-of-principle studies demonstrate the potential of topological conductors for post-Cu interconnect applications.

INVESTIGATION OF LOW TEMPERATURE FLUORINE-BASED ETCHING OF TaN WITH SELECTIVITY TO SIOCH DIELECTRIC

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The industry shift from SiO₂ and Al as the respective dielectric and conductor within the back-end-of-the-line (BEOL) interconnect superstructure to SiOCH low-κ dielectric and Cu improved key metrics such as RC delay, but had inherent integration challenges, one of which was Cu diffusion into the SiOCH film. The solution was the use of diffusion barriers, which are required in current integration schemes to prevent Cu diffusion. TaN is a key diffusion barrier candidate because of strong dielectric adhesion and low inplane resistivity properties at 2-3 nm thicknesses. Creation of the BEOL interconnect superstructure is completed in cycles to create each metal level, requiring the repeated selective removal of not only Cu, but the TaN diffusion barrier, selective to the SiOCH.

SiOCH films attain dielectric constant values of between 2-3 by incorporating non-polar bonds (Si-CH₃) and pores into their structure, making SiOCH sensitive to abrasive processes like CMP and physical, ion-assisted etching. Radical-dominated, fluorine etching of TaN with respect to SiOCH also comes with challenges because of the high volatility of SiF₂, SiF₄, and CF_x SiOCH etch byproducts. We have previously explored methods to accomplish radical fluorine etching of TaN with selectivity to SiOCH as an alternative landing process on the SiOCH, for BEOL integration. Selective deposition of an SiOF film on the SiOCH film compared to the TaN, while etching the TaN, allowed the selective etching of TaN with respect to SiOCH. In this work, we explore the use of radical NF₃/SiF₄ discharges, without O₂ addition (1), at sub-zero sample temperatures ranging from -45 °C to 0 °C (2). In this investigation, we seek to reduce the possibility of SiOCH damage by (1) removal of radical and atomic O interaction with the SiOCH film and (2) reduce the diffusion path for F to limit F diffusion through any SiFx deposition on the SiOCH, and to limit F diffusion within the SiOCH structure itself. We also explore selective removal of any remaining deposition on the SiOCH surface post-processing using plasma-free cleaning to confirm SiOCH integrity. Ex-situ spectroscopic ellipsometry is utilized to characterize film thickness changes after processing in addition to characterization of changes in film refractive index. *Ex-situ* X-ray photoelectron spectroscopy is used to probe the sample surface to characterize surface film properties, while ex-situ Fourier-transform infrared spectroscopy is used to analyze bulk SiOCH changes in the SiOCH bonding structure. Though this multi-modal investigation, we gain insight in the mitigation of SiOCH damage using an O2-free, low temperature process to selectively remove TaN.

ELECTRONIC STRUCTURE, SECONDARY ELECTRON YIELDS, AND CHEMICAL TRANSFORMATIONS OF PHOTORESIST MATERIALS

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Future patterning strategies rely on High-NA EUV lithography, to achieve next generation dimensions of logic chips, with resolution requirements nearing 1 nm. At this scale, each step of the photoresist chemical transformation upon EUV exposure needs to be thoroughly understood, from the generation of secondary electrons by EUV photons, to molecular bond rearrangement upon resist exposure.

As chemical transformations are triggered by low energy electrons, it is crucial to understand and quantify electron energy loss channels in such materials as well as resulting secondary electron yields, as a function of the chemical environment. Additionally, proximity effects with underlayers may become increasingly important with resist film thickness reduction.

We have developed a unique platform able to correlate the yield of secondary electrons generated from incident electrons to the electronic structure and chemical properties of photoresist materials. Surface properties are assessed using x-ray and UV-photoemission spectroscopies, electron energy loss spectroscopy and work function measurements, and are interpreted in the light of ab-initio calculations of the electronic structure.

Two case-studies are considered. First, we study a set of four chemically amplified resist materials, as well as their individual components, using photoemission spectroscopies, electron energy loss spectroscopy and electronic structure calculations. Processes such as chemical transformations, phase segregation and electron energy loss channels are explored as a function of chemical composition. In a second example, we follow the chemical transformation of a polymethyl methacrylate film exposed to electrons using similar spectroscopic methods and additionally correlate the film's alteration to secondary electron yield evolution.

DEGRADATION OF ORGANOSILICON-FUNCTIONALIZED SURFACES DURING X-RAY PHOTOELECTRON SPECTROSCOPY: AN IN-DEPTH STUDY

Anthony Valenti¹, Kandabara Tapily², Kai-Hung Yu², Steve Consiglio², Cory Wajda², Robert Clark², Gert Leusink², Christophe Vallée¹, and Carl A. Ventrice, Jr.¹

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Organosilicon precursors are often used to functionalize and passivate the SiO₂ regions of a substrate during area selective deposition. When using X-ray photoelectron organosilicon-passivated SiO₂ surfaces, spectroscopy (XPS) to characterize decomposition and/or desorption of the organosilicon functionalization can occur. The rate at which this degradation occurs depends on a variety of factors, such as the charge compensation technique used, the spot size of the X-ray beam, and the oxide thickness. In this study, these factors were evaluated for their influence on the degradation rate of the functionalized surface. Two different substrates were studied: 1,000 Å thermal oxide SiO₂/Si(100) and 10 Å plasma oxide SiO₂/Si(100). The surfaces of these substrates were first exposed to water vapor to hydroxylate the surface, followed by exposure to the organosilicon precursor N-(trimethylsilyl)dimethylamine (TMSDMA) to form a trimethylsilyl surface termination. Angle-resolved XPS analysis was performed on each sample type using either no charge compensation, electron beam compensation, ion beam compensation, or dual electron/ion beam compensation. The relative coverage of the trimethylsilyl-functionalization on the oxides surfaces were assessed by measuring the intensity of the O-Si(CH₃)₃ signal in the XPS Si-2p region and by performing water contact angle measurements to determine the change in hydrophobicity of the surfaces. This investigation found that the degredation rate during XPS measurement for the samples with the 1,000 Å oxide was similar for all charge compensation techniques, including no compensation. On the other hand, the 10 Å oxide samples had a much lower degradation rate, except when ion beam compensation or dual beam compensation was used during the XPS measurements. The mechanisms for the differences in degradation rates of samples will be discussed in detail in this presentation.

TOWARD SCALABLE TELECOM SINGLE-PHOTON EMITTERS FOR QUANTUM PHOTONICS

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The most efficient and scalable method for generating and detecting quantum information involves creating an integrated photonic platform that hosts a variety of compact on-chip quantum devices. Single-photon emitters (SPEs) associated with point defects and ions in semiconductors are currently seen as vital resources for many quantum photonic integrated circuit (PIC) applications. A key challenge in scaling SPEs from lab environments to long-distance fiber-based quantum networks is developing SPEs that operate in the near-infrared (NIR) telecom O to C wavelength range, which aligns with low propagation loss in fiber communication channels. The scalability of SPEs is hindered by limited material platforms, strict fabrication and operational temperature requirements (e.g., ≥77K, room temperature), and the random nature of current telecom SPE emitters, which complicate their fabrication and efficient coupling to other photonic devices and PIC chips. Furthermore, the absence of electrically driven SPEs in the telecom regime presents significant obstacles to developing telecom quantum LEDs (QLEDs). These critical scientific and technological challenges have yet to be addressed on a single platform, highlighting the complexity and importance of ongoing research to develop future scalable and compact quantum devices at telecom wavelengths. In this context, we present nanophotonic structures composed of arrays of silicon carbide (SiC) nanowires (NWs) and nanopillars (NPs) based on a novel and fabrication-compatible nanofabrication process. These structures enable the precise placement of erbium (Er³⁺) ions with state-of-the-art accuracy of ~10 nm and allow for the engineering of the optical properties of Er³⁺ ions. The structures exhibit high Er³⁺ photoluminescence (PL) excitation and emission efficiency at 1.54 µm, with near-radiative-limited lifetimes and an absorption cross-section (\sim 2 × 10⁻¹⁸ cm⁻²) that is two orders of magnitude larger than typical values in rare-earth-doped quantum materials. Additionally, the NWs facilitate polarized Er³⁺ emission, an important property for efficient coupling to optical cavities in quantum photonics applications. To this end, we simulate preliminary plasmonic cavity designs for Purcell enhancement of the 1.54 µm Er emission. Finally, we demonstrate a controlled approach to control the number of Er ions utilizing precise control over nanostructure geometry and implantation engineering, towards achieving the isolation of a single Er emitter.

MANGANESE BORIDE AS POTENTIAL INTERCONNECT METAL

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Mn₂B thin films are deposited by combined d.c. and r.f. co-sputtering from Mn and B targets in order to explore the potential of this new conductor as interconnect material. Mn₂B belongs to a group of transition metal borides which have been predicted to be promising directional conductors for scaled interconnects, with a predicted conductivity in the limit of narrow wires that outperforms Cu by ~2x for the [001] crystalline direction of Mn₂B. This study explores the deposition conditions for Mn₂B films on Al₂O₃(0001), $Al_2O_3(\bar{1}\bar{1}20)$ and $Al_2O_3(1\bar{1}02)$ substrates as a function of temperature $T_s = 400-800$ °C and relative power P_{Mn} and P_{B} to the Mn and B targets, with the goal to achieve phasepure, stoichiometric Mn₂B with a single orientation such that transport along [001] can be measured. X-ray diffraction (XRD) and energy dispersive spectroscopy (EDS) analyses indicate that the composition is a function of both T_s and the power to the Mn target, controlling the evaporation rate from the substrate and the deposition flux, respectively. $T_s = 400$ °C leads to a low crystalline quality and $T_s = 800$ °C causes dewetting and discontinuous films. Optimal deposition conditions include $T_s = 700$ °C and 30 W dc and 100 W rf power applied to the Mn and B sources, respectively, resulting in polycrystalline Mn₂B films on Al₂O₃(0001) and Al₂O₃($\bar{1}\bar{1}20$) substrates with minimal impurity phases. Successful growth of epitaxial Mn₂B(100) layers with a single orientation is achieved on $Al_2O_3(1\bar{1}02)$ at 700 °C with $P_{Mn} = 30$ or 40 W. However, XRD analyses indicate that the former contains grains of MnB and the latter Mn impurity phases. The epitaxy is confirmed by a 1.4° full-width at half-maximum (FWHM) of the ω -rocking curve in combination with φ -scans indicating Mn₂B(100) || Al₂O₃(1 $\bar{1}$ 02) and Mn₂B[010] || Al₂O₃[1101]. The next steps involve growing phase pure epitaxial Mn₂B(100) layers and quantifying their resistivity size effect.

MEASUREMENT OF THE DYNAMIC RESPONSE OF MEMS CANTILEVERS

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Microelectromechanical systems (MEMS) are micron scale devices with moving parts. In particular, Menlo Micro produces radio frequency MEMS-based switches that have higher performance than conventional semiconductor-based switches. The MEMS switches use an electrostatically controlled cantilever that is made from a Au-Ni alloy. The electrical contacts of the switch are coated with ruthenium because of its resistance to oxidation at elevated temperatures. In addition, the most stable stoichiometry of ruthenium oxide is RuO₂, which is an electrically conductive oxide. The MEMS devices are encapsulated in a predominately N₂ gas. During operation, it is important that the cantilever does not strike the contact at a velocity that might damage the protective RuO₂ overlayer. The goal of this project was to experimentally determine the resonant frequency and damping coefficient of the cantilever under different gas environments. A high vacuum chamber was modified to allow the insertion of a MEMS test module and the introduction of either N₂, compressed dry air, or Ar into the chamber at various pressures. Our measurements showed that Ar provided the largest damping ratio. In addition, measurements were made of each cantilever's dimensions. The thicker cantilevers had the highest resonant frequencies.

ANGLE-RESOLVED PHOTOLUMINESCENCE SPECTROSCOPY OF ERBIUM-IMPLANTED THIN-FILMS AND NANOPHOTONIC STRUCTURES

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In recent years, erbium-doped materials have attracted considerable attention as platforms for emerging telecom quantum information and integrated photonic technologies, primarily due to the key electronic transition of Er³⁺ around 1532 nm. Controlling the directionality and polarization of light emission from these materials and associated nanophotonic structures is critical for advancing efficient photonic, optoelectronic, and quantum optical devices. In this research, we applied fundamental physics and engineering principles to develop an experimental system capable of performing angle-resolved photoluminescence (PL) studies on erbium-implanted thin films (e.g., silicon carbide and lithium niobate) and nanophotonic structures. This system, designed to control both excitation and emission angles, enables angle-resolved PL measurements across the visible and near-infrared spectra. Leveraging this capability, we systematically studied the angle-resolved PL of erbium-doped thin films. Additionally, we established a clear relationship between the laser excitation angle and the angular distribution of Er PL emission, corroborating these experimental results with finitedifference time-domain (FDTD) simulations. Our findings demonstrate that angleresolved PL microscopy can yield valuable insights into the emission characteristics of films and nanophotonic structures, which are crucial for the development of integrated photonic circuits.

ENHANCING PLASMONIC PHOTODETECTORS: A STUDY ON THE FABRICATION AND OPTICAL BEHAVIOR OF NANOANTENNAS

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The development of plasmonic nanoantennas has gained significant attention in recent years due to their unique ability to collect and focus light into nanoscale volumes. Nanoantennas, typically made from noble metals such as gold, copper, or silver, exhibit localized surface plasmon resonances (LSPRs) that enhance electromagnetic fields at the nanoscale. These resonances are sensitive to the size, thickness, geometric design, composition, and local environment of the nanoantennas. Potential applications include sensing, imaging, photocatalysis and energy harvesting.

In this work, we evaluate the top-down lithography methods such as electron beam lithography and photolithography to fabricate electrically interconnected nanoantennas on glass substrates. We utilize atomic layer deposition (ALD) to deposit thin film of aluminum oxide (Al₂O₃) and titanium dioxide (TiO₂) into PMMA photoresist-patterned surfaces to form photodetectors. We discuss deposition temperature, deposited thickness, and window size effects on ALD applied to e-beam patterned PMMA.

We employ simulation methods such as finite-difference time-domain (FDTD) to model the optical responses of the nanoantennas. The simulated results are then compared with the experimental data from the fabricated samples to validate our findings. The optical behavior of the fabricated features is characterized using spectroscopy techniques, optical extinction measurements. The results demonstrate a strong dependence of LSPR on the geometrical parameters of the nanoantennas, such as shape, size, and lattice spacing. We also measure the photocurrent generated by the nanoantennas under illumination, demonstrating their effectiveness in converting light into electrical signals. Moreover, we assess the potential of these nanoantennas to enhance light-matter interactions for applications in sensing and energy conversion.

DEVELOPMENT OF QUANTUM DOT SCINTILLATOR WITH INTEGRATED PHOTODETECTOR FOR ENHANCED RADIATION DETECTION

<u>Gyana Biswal¹</u>, Tushar Mahajan¹, Michael Yakimov¹, Vadim Tokranov¹, Michael Hedges², Pavel Murat², and Serge Oktyabrsky¹

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High-speed, high-efficiency radiation detectors are essential for a wide range of applications, such as medical imaging, high energy physics experiments, and homeland security. Here we have developed a novel semiconductor-based scintillation detector consisting self-assembled epitaxial InAs quantum dots (QDs) embedded into a GaAs matrix. The semiconductor heterostructure was grown by Molecular beam epitaxy (MBE). The detector consists of a 25 µm thick scintillator released from the substrate with a monolithically integrated InGaAs p-i-n photodiode (PD) for improved optical coupling. Technology for a thin layer transfer and boding is developed for low-parasitics integration of the detector with front-end electronics. Measurements of responses from ²⁴¹Am alpha particles demonstrate collected charge in the range of 30-50 photoelectrons per 1 keV of the deposited energy, or ~13-20% of the theoretically achievable light yield of 240 el./keV in GaAs. The scintillation response shows an extremely fast 0.3-0.6 ns decay constant and about 40-70 ps time resolution for alpha particles, limited by the system noise. Experiments conducted in a vacuum are used to evaluate the detector's energy resolution limits. The combined decay time and light yield make the InAs/GaAs QD heterostructures the fastest high-yield scintillation material reported.

CuAl₂ AS CONDUCTOR FOR HIGH-CONDUCTIVITY INTERCONNECTS

Zahra Ahmadian¹, Ching-Tzu Chen², Atharv Jog³, Daniel Gall¹

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CuAl₂ is explored as possible Cu replacement for narrow high-conductivity interconnects, focusing on the possibility of liner/barrier-free CuAl₂ metallization. Epitaxial 70-300 nm thick CuAl₂(110) and CuAl₂(001) films are deposited on MgO(001) substrates by cosputtering from Cu and Al targets at $T_s = 100$ -400 °C. X-ray diffraction confirms single-phase tetragonal CuAl₂ with a preferred 110 or 001 orientation for $T_s = 100$ or 300-400 °C, respectively. Figure 1 shows the x-ray diffraction (XRD) patterns for these films. For an Al/Cu ratio of 2.1, the XRD pattern displays a dominant CuAl₂ 110 peak with no other misoriented grains nor secondary phases. However, as the Al content increases, an additional Al 200 peak appears at $2\theta = 44.75^{\circ}$, indicating the presence of excess Al.

Resistivity measurements are performed at room temperature (295 K) and in liquid nitrogen (77 K). The room temperature resistivity varies between 9.4 and 10.9 $\mu\Omega$ ·cm for Al/Cu ratios between 1.8 and 2.1, with the lowest value of 9.4 $\mu\Omega$ ·cm for the stoichiometric Al/Cu ratio of 2.0. Increasing the Al/Cu ratio to 2.3 leads to a resistivity drop to 8.4 $\mu\Omega$ ·cm which is caused by the conductive Al impurity phase, consistent with XRD results.

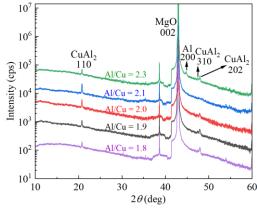


Figure 1: θ -2 θ x-ray diffraction patterns from CuAl₂/MgO(001) layers with Al-to-Cu ratios Cu/Al = 1.8-2.3